

WEST Search History

DATE: Thursday, July 01, 2004

Hide?	Set Name	Query	Hit Count
		<i>DB=USPT,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR</i>	
<input type="checkbox"/>	L10	L9.clm.	31
<input type="checkbox"/>	L9	l3 with current	379
<input type="checkbox"/>	L8	l3 same current	503
<input type="checkbox"/>	L7	((detect\$4 or sens\$4) near3 workload\$4) same (monitor\$5 near3 current near3 (sens\$7 or detect\$4))	0
<input type="checkbox"/>	L6	(increas\$4 near5 workload\$4) same (monitor\$5 near3 current near3 (sens\$7 or detect\$4))	0
<input type="checkbox"/>	L5	(increas\$4 near3 workload\$4) same (monitor\$5 near3 current near3 (sens\$7 or detect\$4))	0
<input type="checkbox"/>	L4	L3 same (monitor\$5 near3 current near3 (sens\$7 or detect\$4))	11
<input type="checkbox"/>	L3	((detect\$4 or sens\$4) near3 increas\$4 near3 (load or task or work\$4))	2170
<input type="checkbox"/>	L2	L1 same ((detect\$4 or sens\$4) near3 current)	1
<input type="checkbox"/>	L1	(increas\$7 near3 clock\$4 near2 (speed or rate or frequency)) with ((based or respons\$4 or depend\$4 or accord\$5) near3 current)	11

END OF SEARCH HISTORY

BEST AVAILABLE COPY

[First Hit](#) [Fwd Refs](#)[Previous Doc](#)[Next Doc](#)[Go to Doc#](#)

End of Result Set



Generate Collection

Print

L2: Entry 1 of 1

File: USPT

Sep 29, 1998

DOCUMENT-IDENTIFIER: US 5814903 A

TITLE: Programmable gain for switched power control

Brief Summary Text (17):

In a particular embodiment of the invention, the clock rate control arrangement includes a sensor for sensing the current of at least one of the current sources, and for generating the clock rate control signals in response to the current. In one version of this embodiment, the rate of the clock signals tends to increase in response to diminution of the source current, and tends to decrease in response to increase of the source current. The clock rate control arrangement may include an alternating signal sensor coupled to the output port of the filter, for sensing a signal (preferably a voltage) having a rate of change or slope for sensing an alternating signal, and for producing the clock rate control signals in response to the rate of change of the voltage and/or of the alternating signal. A high-pass or bandpass filter is coupled to the load, and has its output port coupled to the alternating signal sensor, for coupling to its output port only an alternating component of the voltage across the load. *not work load*

Detailed Description Text (11):

In a particular embodiment of the invention, the clock rate control arrangement (612, 614; 810, 812) includes a sensor (614) for sensing the current of at least one of the current sources (230a, 230b, . . . , 230h), and for generating the clock rate control signals in response to the current. In one version of this embodiment, the rate of the clock signals tends to increase in response to diminution of the source current, and tends to decrease in response to increase of the source current. The clock rate control arrangement (612, 614; 810, 812) may include an alternating signal sensor (614) coupled to the output port of the filter (216), for sensing a signal (preferably a voltage) having a rate of change (slope) or for sensing an alternating signal (as opposed to a direct signal), and for producing the clock rate control signals in response to the rate of change of the voltage and/or of the alternating signal. A highpass or bandpass filter (216) is coupled to the load (40), and has its output port coupled to the alternating signal sensor (614), for coupling to its output port only an alternating component of the voltage across the load. It should be noted that a highpass filter includes a high-pass section, in that it blocks passage of direct components of the signal.

[Previous Doc](#)[Next Doc](#)[Go to Doc#](#)

BEST AVAILABLE COPY